(PATENT)

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Regnier et al.

Application No.: 10/589,275

Filed: August 10, 2006

For: PROCESS FOR THE FABRICATING AN

ELECTRONIC INTEGRATED CIRCUIT AND ELECTRONIC INTEGRATED

CIRCUIT THUS OBTAINED

Confirmation No.: 5699

Art Unit: 2811

Examiner: J. Im

## **RESPONSE TO OFFICE ACTION**

O.K to enter.
7/28/2010 /jmi/

MS AMENDMENT Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In response to the final Office Action dated May 26, 2010, please consider the following claims and remarks: